

(ch4.)

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1

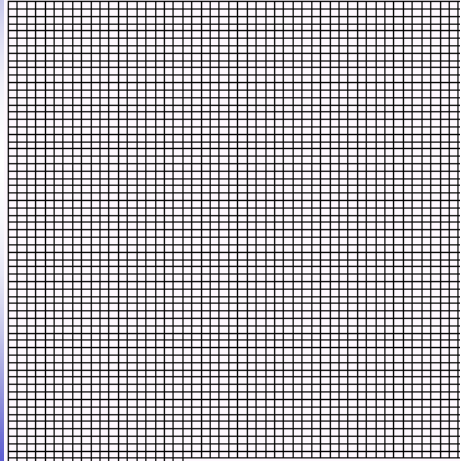
4.1 () - #0.

1) PSU 가	1/2 (DUAL PSU)
2)	1 ---->
3)	1 ---->
4) 89C51 CPU	1
40	1
5) 8 DIP SW	1
6) X - TAL (12MHz)	1
7) LED	8
8) (470ohm)	8
9) (8.2kohm)	1
10) (20pF)	2
11) (10uF)	1

2

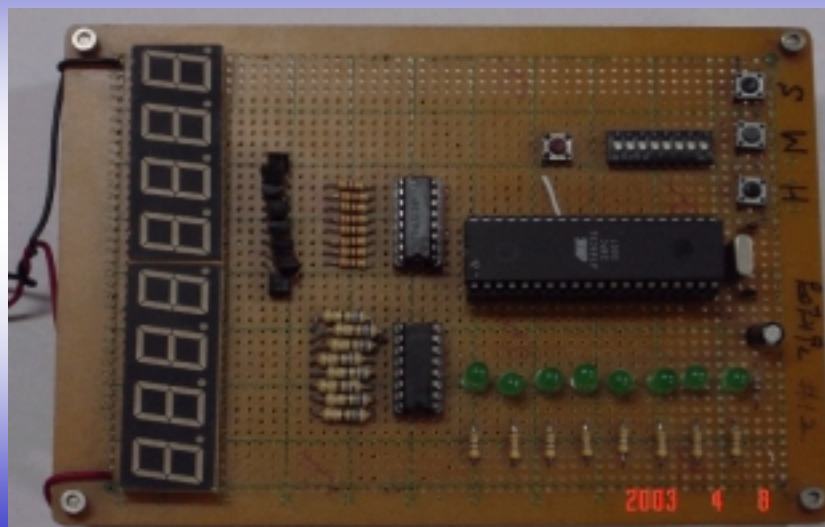
4.1 () - #0.

만능기판 실체 배선도



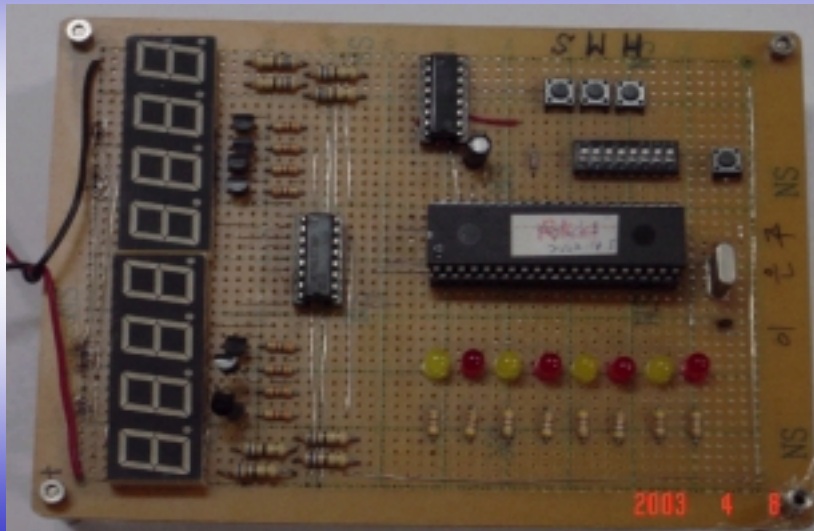
3

4.1 (- DIP) - #0. 4 Soldering (1)



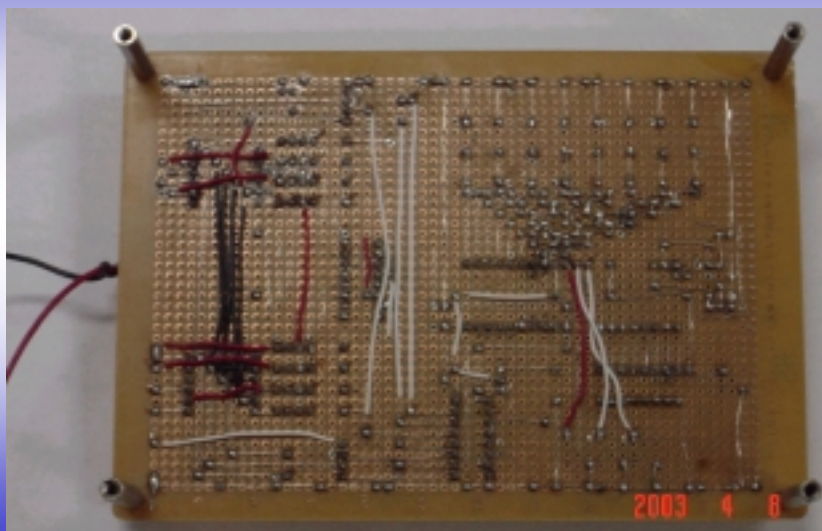
4

4.1 (- DIP)
- #0.4 Soldering (2)



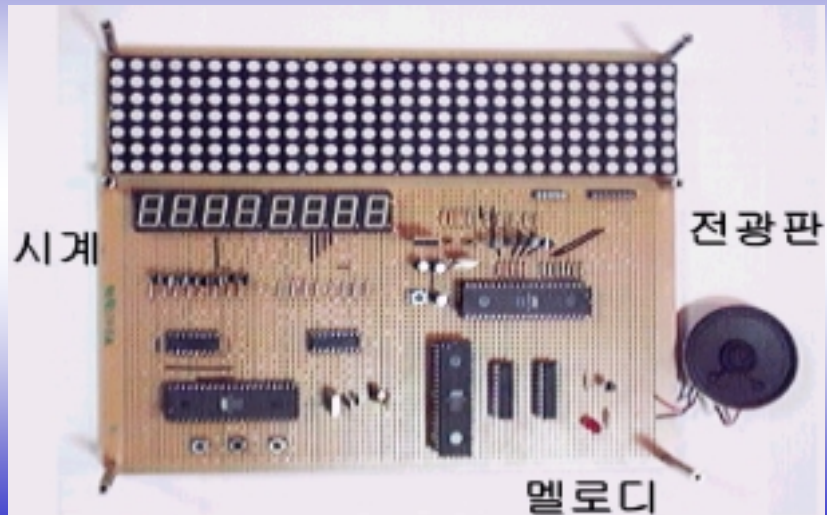
5

4.1 (- DIP)
- #0.4 Soldering (3)



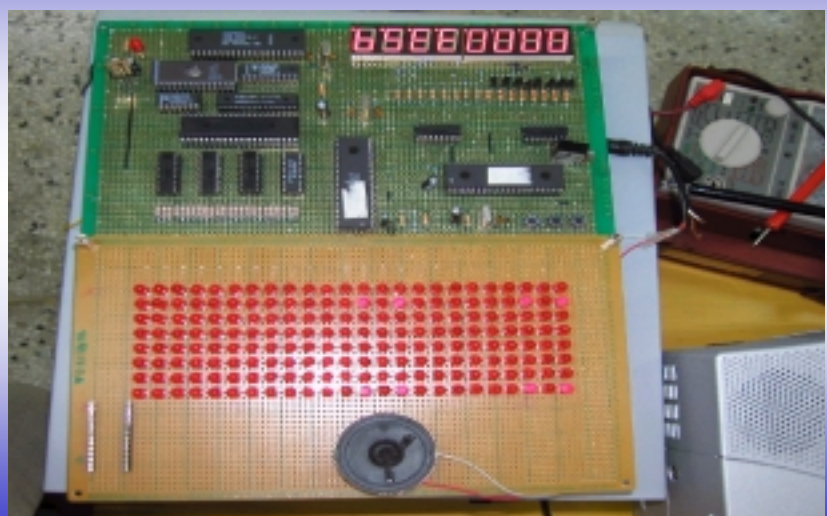
6

4.1 (- DIP)
- #0. 4 Soldering (4)



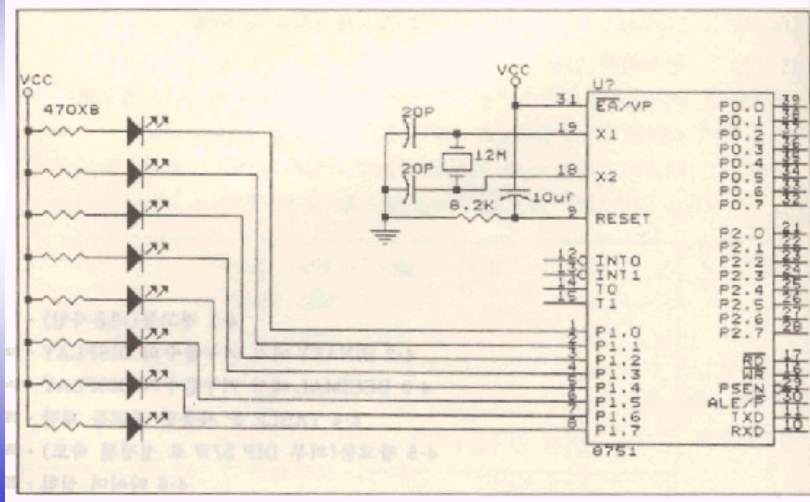
7

4.1 (- DIP)
- #0. 4 Soldering (5)



8

4.1 () - #1.



9

4.2 () - #2.

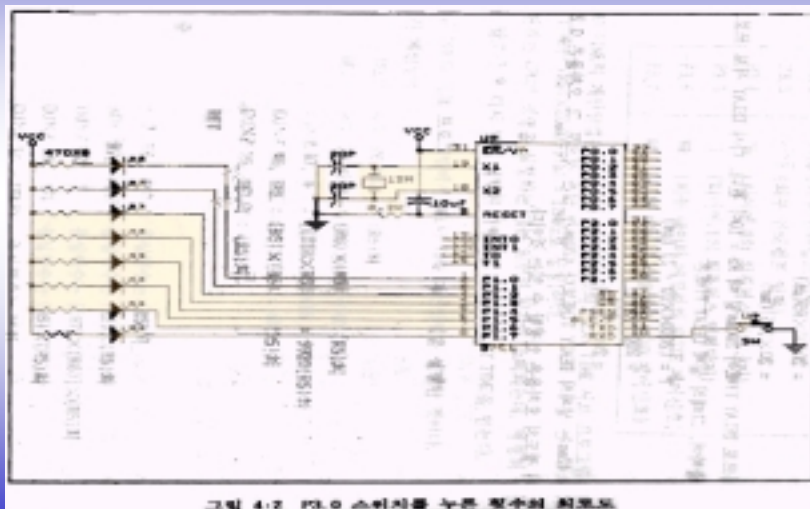
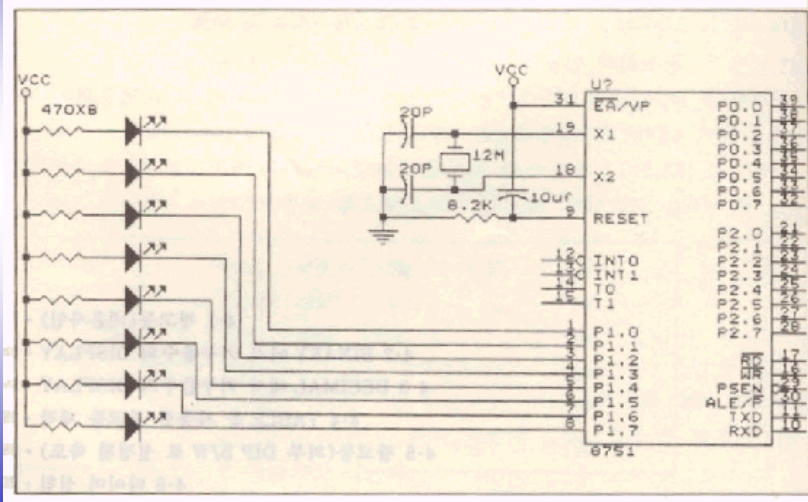


그림 4.2 P3.0 스위치를 누른 직후의 회로도

10

4.4 (Table #3.)



11

4.5 (- DIP #4.)

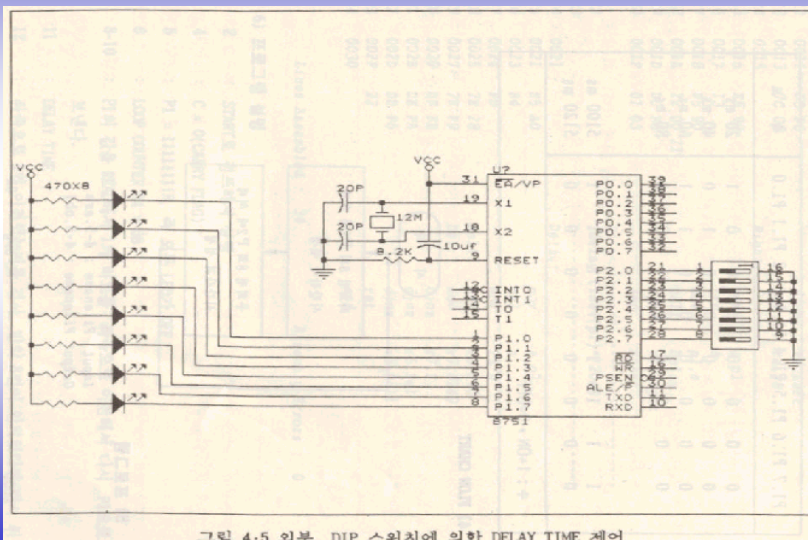


그림 4-5 외부 DIP 스위치에 의한 DELAY TIME 제어

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